



International Conference on Electrical, Electronics & Optimizations Techniques

3rd – 5th March, 2016 | Tamil Nadu, India.

Session Title - State of Art Progress in MEMS and VLSI Design (SAPMVD)

Theme and aim of the special section:

New concepts and reporting breakthroughs in MEMS and VLSI designs.

Topics range from deep sub microns CMOS transistors, other material based transistors like Graphene FETs, CNT FETs, etc. The track will cover basic technologies, to passives and actives components to system over a wide range of frequencies including VHF, UHF, RF, microwave, millimeter-wave, terahertz, and optical. The session will also encircle the up-to-date research in RFIC, MIC, and Microsystems technologies, advances in CAD, modeling, EM simulation, wireless systems, RFID and related fields.

Topics:

Topics of interest include, but are not limited to, the following

1. Behavioural Modelling of MEMS based Design.
2. Reliability of Interconnects, Digital/Analog/Mixed Signal Circuit/MEMS Circuits.
3. CNT and Graphene based Microwave circuits like LNA, PLL, VCO, Transmitters, Transmission lines, RF Connectors, Receiver, Tuner, RF Filters, Power Amplifier etc.
4. Neomorphonic VLSI system (FPGA/Chips)
5. Power Distribution Circuits in VLSI/MEMS.
6. RFIC implementations through Analog Circuits.
7. Biosensors/ Biomems Implementation through CMOS Circuits.
8. Software Defined Radio/ Cognitive Radio.
9. Verification and Testing of Digital/Analog/Mixed Signal Circuit/MEMS Circuits
10. Antenna Synthesis using Heuristic Algorithms/Neural Networks/ Fuzzy Logics.
11. Big Data Analytics in VLSI/MEMS Design Automation.
12. Internets of things (IOC) in VLSI/MEMS
13. Reconfigurable computing.
14. RFMEMS design and modeling

Special Session Coordinator Details

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Special Session Title: Session Title - State of Art Progress in MEMS and VLSI Design (SAPMVD)

Website: www.iceeot.org

All papers will be published by IEEE Xplore and will be index by SCOPUS

We are organizing a special session on **“Session Title - State of Art Progress in MEMS and VLSI Design”** in The IEEE International Conference on Electrical, Electronics & Optimizations Techniques (ICEEOT-2016). During 3rd – 5th March, 2016 in the city of Tamil Nadu, India.

Special Session Submissions can only be submitted to the Session Coordinator Mail Id - navaid@gbu.ac.in, navaidrizvi@gmail.com

Publication of Conference Papers (Special Sessions)

All papers will be published by IEEE Xplore. As well as indexed by SCOPUS

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